

## JOB DESCRIPTION

**Job Title:** Software Design Engineer

**Purpose of Job:** PPM is expanding its product portfolio so is strengthening and extending its Shrivenham based development team.

**Location:** Swindon

**Reporting to:** Development Team Lead

### Role:

PPM is designing an innovative new range of RF, microwave, and optical solutions for SatCom, cellular, and defence applications. This role offers the chance to join a collaborative and engaging working environment, where knowledge sharing is actively encouraged; allowing all developer disciplines to cross skill over a range of different technologies. As part of an agile design and development team you will have the opportunity to develop product concepts and see designs through to customer delivery; directly contributing to the company's success. Responsibilities will include:

- Developing code for new open architecture software products
- Assisting with requirements analysis
- Software-hardware integration testing

The successful candidate will demonstrate a proactive and innovative approach to developing technical solutions to complex customer mission problems. They will have a passion for developing high quality software solutions and, through the application of object oriented programming techniques, establish and expand a library of SW IP to be drawn upon by future development projects.

It is *essential* the candidate has:

- Expert in object oriented programming languages i.e. C++, Java, Python
- Experience with component-based coding frameworks for heterogeneous platforms i.e. OpenCPI, OpenCL, CUDA
- Familiarity with the Agile development methodology, and use of task management tools i.e. Tuleap / Jira
- Equally comfortable developing code for hardware, middleware, and application layers
- Experience coding a variety of hardware platforms i.e. FPGA, DSP, GPU, CPU, & PIC.
- Demonstrable interpersonal and team communication skills, high ethical standards, and personal accountability.

It is *desirable* the candidate has:

- Experience and demonstrated competence with at least one of the following languages: VHDL and Verilog.
- Demonstrable knowledge of applying VHDL/Verilog on an FPGA device.
- Experience with high level languages i.e. LabView, Perl

### Person:

The successful candidate will work within a multi-disciplined engineering team and will be expected to use their own interpersonal skills to draw upon existing internal expertise, from product designers, mechanical engineers, software developers, photonics experts, and system design specialists. They will also present a confident and collaborative interface when dealing with colleagues, sub-contractors, and suppliers, through either written or verbal communications.

In addition to meeting customer's needs, solutions will be required to exceed the company's documented quality standards and be delivered within agreed timescales and budgets. The successful candidate will be capable of applying themselves to multiple development projects simultaneously and be comfortable responding to changing business priorities as they arise.

**Required behaviours:**

- *Creativity and innovation* – Possessing a passion for driving continuous improvement by spotting opportunities and seeking the views of others
- *Keeping Commitments* – You demonstrate a can-do attitude and you drive to complete tasks and projects and report that they are completed, even when other opportunities arise which appear to be more interesting
- *Collaboration and Communication* – ability to develop relationships with colleagues outside your department built on mutual trust, respect and drive for sharing and success
- *Impact and influence* – You listen to different perspectives, evaluate, persuade and carefully shape your work to deliver excellent results
- *Teaching and Sharing* – You share what you know with others and seek to learn from the vast pool of knowledge in others at PPM
- *Company Focus* – Put the needs of the company first before your team needs, drive to achieve the goals of the company as well as your own team